

AMENDMENTS TO THE SPECIFICATION

Please replace Paragraph [0024] with the following paragraph rewritten in amendment format:

[0024] The imager 200 includes a first plurality of photo-sensing pixels, the majority of which can be classified as a first type of photo-sensing pixel 202 (hereafter also referred to as a Type I pixel). The first plurality further includes pixels that can be classified as a second type of photo-sensing pixel 204 (hereafter also referred to as Type II pixel). The Type I pixels 202 can be thought of as non-sampling pixels. In contrast, the Type II pixels can be thought of as sampling pixels. Physically, the Type I pixels 202 and the ~~Type~~Type II pixels 204 are the same but for the addressing and control lines going ~~the~~to them, respectively, which give rise to their different classifications. More about the differences between Type I (non-sampling) and Type II (sampling) pixels will be discussed below. Alternatively, additional types of pixels can be provided on the image sensor 200.

Please replace Paragraph [0027] with the following paragraph rewritten in amendment format:

[0027] Each bank ~~204~~204_i is organized into arrays, each array being either a sampling array 216 or a non-sampling array 218. A sampling array 216 includes a Type II pixel 204 at the bottom, closest to the corresponding HCCD 208_i. The other pixels in

the array 216 are Type I pixels 202. The non-sampling array 218 includes Type I pixels 202 but does not include any Type II pixels 204.

Please replace Paragraph [0033] with the following paragraph rewritten in amendment format:

[0033] In the sampling mode, the array 300 is controllable so that only the information in the Type II pixel 202 is sampled/read. The information in the Type I pixels 304-316 is not read in the sampling mode. Depending upon the value of the sample read from the Type II pixel 302, the array 300 is controllable in a read-mode (more detail to be discussed below in terms of Figs. 4A-4D) so that the information in the Type I pixels 304-316 is read. The term "controllable" is used here to connote that the imager 200, particularly the array 300, is configured with clocking lines and address/control lines so that the clocking circuit 108 and control logic, e.g., in the ASIC 106, respectively, can control the array 300 to behave according to the sampling-mode or the read-mode.

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